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APPLICANTS

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ES

** CONTINUING DATA *****

This application is a 371 of PCT/GB00/02321 06/15/2000

** FOREIGN APPLICATIONS *****

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>ED GARCIA-ORTEGA</i>	INITIALS <i>EG</i>		

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TITLE

Semiconductor device simulation method and simulator

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